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Terms	Documents
L3 and ((configur\$3 or plac\$3) near10 bus)	29

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<u>L4</u>	L3 and ((configur\$3 or plac\$3) near10 bus)	29	<u>L4</u>
<u>L3</u>	l1 and L2	107	<u>L3</u>
<u>L2</u>	((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))	30975	<u>L2</u>
<u>L1</u>	monitor\$3 same (activity or utiliz\$5) same bus	1864	<u>L1</u>

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L4: Entry 1 of 29

File: PGPB

Mar 1, 2007

PGPUB-DOCUMENT-NUMBER: 20070050653

PGPUB-FILING-TYPE:

DOCUMENT-IDENTIFIER: US 20070050653 A1

TITLE: System and method for information handling system adaptive variable bus idle timer

PUBLICATION-DATE: March 1, 2007

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Verdun; Gary	Georgetown	TX	US

APPL-NO: 11/215260 [\[PALM\]](#)

DATE FILED: August 29, 2005

INT-CL-PUBLISHED:

TYPE	IPC	DATE	IPC-OLD
IPCP	G06F1/26	20060101	G06F001/26

INT-CL-CURRENT:

TYPE	IPC	DATE
CIPP	G06 F 1/26	20060101

US-CL-PUBLISHED: 713/320

US-CL-CURRENT: [713/320](#)

ABSTRACT:

Power management of an information handling system PCI Express bus dynamically adjusts the inactivity time at the bus that is determined before initiation of a low power state by analyzing the transitions between low power and operating states over time. Dwell times of the bus in the low power state are compared with an inactivity goal to determine if the inactivity time should be adjusted up, such as when the bus enters the low power state too often, or should be adjusted down, such as when the bus enters the low power state too infrequently. In one embodiment, the dwell time is the time from entry into a low power state until initiation of the transition to an operating state and the inactivity goal is the time required for the bus to enter and exit the low power state.

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<u>L8</u>	L7 and ((configur\$3 or plac\$3) near10 bus)	52	<u>L8</u>
<u>L7</u>	l5 and L6	260	<u>L7</u>
<u>L6</u>	((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))	136284	<u>L6</u>
<u>L5</u>	monitor\$3 same (activity or utiliz\$5) same bus	6721	<u>L5</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
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<u>L3</u>	l1 and L2	107	<u>L3</u>
<u>L2</u>	((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))	30975	<u>L2</u>
<u>L1</u>	monitor\$3 same (activity or utiliz\$5) same bus	1864	<u>L1</u>

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Terms	Documents
(370/420 370/437 370/354 370/229 370/235 370/237 709/235 709/239 709/240 710/305 710/306 710/107 710/309 710/316 711/147 713/300 713/320 714/47).ccls.	17255

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L9 710/305,306,107,309,316;713/300,320;714/47;709/235,239,240;370/420,437,354,229,235,237;71

L8 L7 and ((configur\$3 or plac\$3) near10 bus)

L7 l5 and L6

L6 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L5 monitor\$3 same (activity or utiliz\$5) same bus

DB=PGPB; PLUR=YES; OP=OR

L4 L3 and ((configur\$3 or plac\$3) near10 bus)

L3 l1 and L2

L2 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L1 monitor\$3 same (activity or utiliz\$5) same bus

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L10 L8 and L9

L9 710/305,306,107,309,316;713/300,320;714/47;709/235,239,240;370/420,437,354,229,235,237;71

L8 L7 and ((configur\$3 or plac\$3) near10 bus)

L7 L5 and L6

L6 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L5 monitor\$3 same (activity or utiliz\$5) same bus

DB=PGPB; PLUR=YES; OP=OR

L4 L3 and ((configur\$3 or plac\$3) near10 bus)

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Human activities monitoring at bus stops**
Gasserm G; Bird, N.; Masoud, O.; Papanikolopoulos, N.;
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Trace driven modelling and performance evaluation of tightly coupled multiprocessor systems

Luc, K.-Q. Ong, S. Hu, E.C.
IBM Thomas J. Watson Res. Center, Yorktown Heights, NY, USA;
This paper appears in: Computer Design: VLSI in Computers and Processors, 1988. ICCD '88.,
Proceedings of the 1988 IEEE International Conference on
Publication Date: 3-5 Oct. 1988
On page(s): 533 - 536
Meeting Date: 10/03/1988 - 10/05/1988
Location: Rye Brook, NY
INSPEC Accession Number: 3305166
Digital Object Identifier: 10.1109/ICCD.1988.25756
Posted online: 2002-08-06 16:01:25.0

Abstract
The development and comparison of two trace-driven simulation models for microsystems with tightly coupled, shared-bus multiprocessors is presented. One model monitors the complete activities of processors, private caches, global bus, and main memory, while the other first abstracts local bus activities of processors, and then takes care of the multiprocessing interaction. The second model provides a means to move more quickly within the design space, while the first model can be used to select a final optimized design. Examples are presented to illustrate how these simulation models can help a complex choice among architectures, system configurations, and chip parameters for the design of an optimized microsystem

- Index Terms
Inspec
Controlled Indexing
digital simulation parallel architectures performance evaluation
Non-controlled Indexing
local bus activities microsystems multiprocessing interaction performance evaluation
shared-bus multiprocessors tightly coupled multiprocessor systems trace-driven simulation models
Author Keywords
Not Available

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	60	reserve bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:42
L2	4280	alternate path	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:43
L3	19	arbitrat\$4 same l2	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L4	9980	second bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L5	3260	different bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L6	67	grant\$4 same l5	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:47
L7	3678	bus b	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:47
L8	2503	bus a	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
L9	1276	l7 and l8	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
L10	207	grant and l9	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
L11	25	("3980993" "4245344" "4511969" "4853846" "4864496" "4897786" "4918597" "4935894" "4982321" "5070443" "5072369" "5073852" "5086499" "5191653" "5245703" "5255374").PN. OR ("5442754").URPN.	US-PGPUB; USPAT; USOCR	ADJ	OFF	2007/05/30 10:52
L12	131	load and l10	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:52
L13	31	("4141067" "4992930" "5058006" "5091846" "5119485" "5155824" "5206941" "5283886" "5317720" "5333296" "5335335" "5341487" "5398325" "5432918" "5434993" "5446863" "5506967").PN. OR ("5737756").URPN.	US-PGPUB; USPAT; USOCR	ADJ	OFF	2007/05/30 10:56

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L15	50	("4245344" "4535448" "4695952" "4796022" "4933835" "4987529" "5124983" "5235684" "5265211" "5276828" "5317716").PN. OR ("5511224").URPN.	US-PGPUB; USPAT; USOCR	ADJ	OFF	2007/05/30 10:59
L16	68	("4245344" "4473880" "4484273" "4490785" "4495569" "4503534" "4503535" "4511959" "4535448" "4607365" "4695952" "4766536" "4979100" "4982321" "5202980"). PN. OR ("5345566").URPN.	US-PGPUB; USPAT; USOCR	ADJ	OFF	2007/05/30 11:02
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